

Press Release

Waalre – 4 November 2009

verum®

Tools for building
mathematically
verified software

ASM International, TASS, ESI and Verum create partnership to improve software development methods

A partnership consisting of ASM International, TASS Software Professionals, the Embedded System Institute and Verum have been awarded government funding for an 18 month project which will help improve the development of software for semiconductor process equipment. The project, coded named “prOVEN” will address two main issues: (1) how to incorporate the ASD method in the development process and (2) how to integrate software which is generated with the ASD tools into systems for controlling semiconductor equipment. The results will include a methodology describing how ASD and the code generated from the ASD:Suite can be incorporated gradually into an existing system.

Robert Howe, CEO Verum commented: *“We have already proved that Verum’s ASD:Suite can reduce the development costs and improve the quality of software for complex systems such as semiconductor process equipment so we’re delighted to participate in this programme. The project will develop the methodology that will make it even easier for world-leading companies like ASML to benefit from using the ASD:Suite.”*

Jan Monsuur, manager Software Development of ASM Europe commented: *“Improving the quality of software requires formal methods that guarantee correct code. By ‘correct’ I mean ‘matching the requirements 100 %’. Until recently, such methods did not exist in industry because they required specialist mathematics. Verum has embedded the maths into their ASD:Suite making this programme an exciting project.”*

The project is focused on the semiconductor process equipment but the results are expected to be applicable to wide range of industries that depend upon embedded systems.

For further details of the other participants:

ASM International NV

<http://www.asm.com/>

TASS Software Professionals

<http://www.tass.nl/>

Embedded System Institute

<http://www.esi.nl/>

ASD:Suite

Verum’s ASD:Suite software design toolset eliminates all behavioural defects from complex software designs, dramatically cutting the cost of software development, testing and maintenance. ASD:Suite automatic code generation further reduces the cost of software development by 30% per delivered line of code. The key difference between Verum’s ASD:Suite and tools that claim to improve productivity is Verum’s Analytical Software Design formal verification technology: ASD allows the design of a software system to be mathematically verified as complete and correct, even before a single line of code has been written.

For more information please contact:

Stephanie Bakker

Email: comunications@verum.com

Tel: +31 (40) 2359090

Verum Consultants BV
Laan van Diepenvoorde 32
5582 LA Waalre
The Netherlands

T +31 (0) 40 2359090
F +31 (0) 40 2359099
E info@verum.com
W www.verum.com