

Press Release

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verum^o

Tools for building
mathematically
verified software

Verum's ASD:Suite improves Sioux's productivity and quality

Following a rigorous evaluation, leading software development company Sioux has decided to license the ASD:Suite. During the evaluation Sioux proved that with the ASD:Suite they could produce higher quality software in shorter timescales.

Sioux Embedded Systems is a major embedded software development and consultancy company based in The Netherlands and Belgium. The company employs 170 people and works with prestigious customers such as ASML, Assembléon, FEI Company, Océ, Philips, Sony and Thomson. It continually searches for ways to improve its processes.

Sioux used an existing software component to compare conventional software development to that using Verum's ASD:Suite. The results showed that the ASD:Suite performed strongly especially in terms of economics, productivity and quality, improving the design and coding productivity by 21%.

"The ASD:Suite helps you to think before you start implementing, so you get a better architecture and avoid unnecessary complexity. It also finds bugs that otherwise would only be discovered during the integration phase. The end result is a higher quality and more easily maintained product" said Ger Schoeber, Executive Consultant at Sioux.

Sioux plans to use ASD as part of its core software development strategy. In future its customers will benefit from higher quality and easier to maintain software. A White Paper describing the results of the Sioux evaluation results will shortly be available on the Verum website.

ASD:Suite

Verum's ASD:Suite software design toolset eliminates all behavioural defects from complex software designs, dramatically cutting the cost of software development, testing and maintenance. ASD:Suite automatic code generation further reduces the cost of software development by 30% per delivered line of code. The key difference between Verum's ASD:Suite and tools that claim to improve productivity is Verum's Analytical Software Design formal verification technology: ASD allows the design of a software system to be mathematically verified as complete and correct, even before a single line of code has been written.

For more information please contact:

Stéphanie Bakker

Email: communications@verum.com

Tel: +31 (40) 2359090

Verum Consultants BV
Laan van Diepenvoorde 32
5582 LA Waalre
The Netherlands